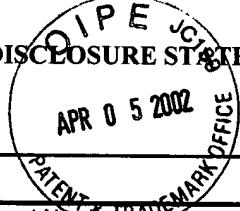


Form PTO-1449
(Rev. 8-83)U.S. Department of Commerce
Patent and Trademark Office

Atty Docket 0756-2434

Serial No. 10/074,050

INFORMATION DISCLOSURE STATEMENT



Applicants: Shunpei YAMAZAKI et al.

Filing Date: February 14, 2002

Group Art Unit: N/A

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
Dh	5,529,937	06/25/1996	Zhang et al.			
	5,608,232	03/04/1997	Yamazaki et al.			
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	5,773,327	06/30/1998	Yamazaki et al.			
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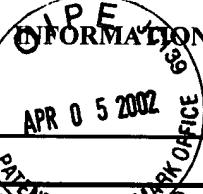
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Applicants: Shunpei YAMAZAKI et al.

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Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
Xe	6,133,119	10/17/2000	Yamazaki			
	6,153,445	11/28/2000	Yamazaki et al.			
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INFORMATION DISCLOSURE STATEMENT

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Applicants: Shunpei YAMAZAKI et al.

Filing Date: February 14, 2002

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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
Yle	05-109737	04/30/1993	JP	—	—	Full Eng

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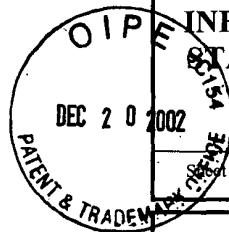
Examiner	Dung Huynh	Date Considered	April - 04
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/074,050
				Filing Date	February 14, 2002
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2812
				Examiner Name	Unassigned
				Attorney Docket Number	0756-2434
Serial No. 2002	1	of	3		



INFORMATION

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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of

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Complete if Known

Application Number	10/074,050
Filing Date	February 14, 2002
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2812
Examiner Name	Unassigned
Attorney Docket Number	0756-2434

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
De	3,535,775			Garfinkel et al.	10/27/1970	
	4,371,403			Ikubo et al.	02/01/1983	
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Examiner Signature		Date Considered	4/04
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Application Number	10/074,050
Filing Date	February 14, 2002
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2812
Examiner Name	Unassigned
Attorney Docket Number	0756-2434

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
Dh		5,646,424		Zhang et al.	07/08/1997	
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		2001/0034088	A1	Nakamura et al.	10/25/2001	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
Dh		EP	0 651 431	A2		05/03/1995		English
		JP	07-130652			05/19/1995		Abstract
		JP	08-078329			03/22/1996		Abstract
		JP	09-074207			03/18/1997		Full
		JP	2000-260777			09/22/2000		Abstract
		JP	2001-210828			08/03/2001		Abstract
Dh		JP	3032801			02/18/2000		Abstract

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Dle		U.S. Patent Application Serial No. 10/051,064, "Semiconductor Device and Method of Manufacturing the Same", Takashi Hamada et al., January 18, 2002.	
		U.S. Patent Application Serial No. 10/056,055, including specification, drawings and filing receipt, "Semiconductor Device and Manufacturing Method of the Same", Osamu Nakamura et al., January 28, 2002.	
		U.S. Patent Application Serial No. 10/097,641, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei Yamazaki et al., March 15, 2002.	
		U.S. Patent Application Serial No. 10/072,931, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed February 12, 2002.	
		U.S. Patent Application Serial No. 10/020,961, including specification, drawings and filing receipt, "Method of Manufacturing Semiconductor Device and Semiconductor Device", Shunpei Yamazaki et al., December 19, 2001.	
		U.S. Patent Application Serial No. 10/066,542, including specification, drawings and filing receipt, "Semiconductor Device and Method for Manufacturing the Same", Osamu Nakamura et al., February 5, 2002.	
		M. Miyake et al., "Characteristics of Buried-Channel pMOS Devices with Shallow Counter-Doped Layers Fabricated Using Channel Preamorphization", IEEE Transactions on Electron Devices, Vol. 43, No. 3, March 1996, pp. 444-449.	
Dle		L. S. Lee et al., "Argon Ion-Implantation on Polysilicon or Amorphous-Silicon for Boron Penetration Suppression in p+ pMOSFET", IEEE Transactions on Electron Devices, Vol. 45, No. 8, August 1998, pp. 1737-1744.	

Examiner Signature	<i>Dle</i>	Date Considered	<i>4/04</i>
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				Application Number	10/074,050
				Filing Date	February 14, 2002
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2812
				Examiner Name	Unassigned
				Attorney Docket Number	0756-2434
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<i>Dh</i>		D.J. Llewellyn et al., <i>Implantation and Annealing of Cu in InP for Electrical Isolation: Microstructural Characterisation</i> , IEEE, 1997, pages 313-316	
<i>Dh</i>		Kevin S. Jones et al., <i>Boron Diffusion Upon Annealing of Laser Thermal Processed Silicon</i> , IEEE, 2000, pages 111-114	

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